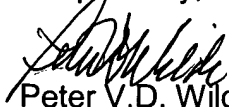


1-27 canceled

28. (currently amended) An integrated circuit having a field-plated resistor the field-plated resistor comprising:

- a. a resistor body formed in a semiconductor substrate, the resistor body having first and second contact regions,
- b. a first insulating layer on the resistor body, the first insulating layer approximately coextensive with the resistor body and having a top surface and a bottom surface,
- c. a contact window in the first insulating layer and extending from the top surface of the first insulating layer through the first insulating layer to the resistor body,
- d. a field plate on the first insulating layer and approximately coextensive therewith and with the resistor body, the field plate having a top surface and a bottom surface, with a portion of the bottom surface extending through the contact window in the first insulating layer and into contact with the first contact region of the resistor,
- e. a second insulating layer, with a first portion of the second insulating layer at least substantially covering the field plate,
- f. an electrical contact extending through the first portion of the second insulating layer to the top surface of the field plate,
- g. an electrical contact to the second contact region of the resistor, and electrically insulated from the field plate, and

Respectfully,

  
Peter V.D. Wilde  
Reg. No. 19658

Date: 01-20-07

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